

Direct AC-AC Full Bridge Converter

Sofiya A¹, Renjith G², Mumthas A³

¹(Asst Prof, Dept. Of EEE, College of Engineering Perumon, Kollam)

²(Asst Prof, Dept. Of EEE, College of Engineering Perumon, Kollam)

³(MTech scholar, Dept. Of EEE, Rajiv Gandhi Institute Of Technology)

Abstract: Induction heating are applied for several industrial and domestic application .Among these application it is mainly applied to domestic field as an induction cooker. In conventional induction heating application, efficiency and cost are compromised due to the conduction loss and increased components. The proposed topology with full bridge converter uses only two diodes reducing conduction loss and a direct ac-ac conversion is possible here. The IGBT is operated by a switching frequency of 150KHz.

I. Introduction

Induction heating is emerging as the most important application in power electronics. .Due to its high temperature it is applicable to steel melting, brazing and surface hardening .Domestic induction heating has become very relevant due to its higher efficiency and the reduced heating time .Induction heating which is a non contact heating technique requires high-frequency current supply that will induce a high frequency eddy current in the working piece that will result in the heating effect. Power semiconductor switching devices, digital and analogue control devices, circuit components and high frequency soft switching inverters are used to bring out the unique advantages of induction heating into practical. Classical induction heating method is based on two stages: a rectifier and resonant inverter. A four-diode full bridge rectifier is used to rectify the main ac voltage and a small value dc-link capacitor to ensure input power factor close to 1.The resonant inverter supply the inductor pot system. The one switch topology is mostly used for low cost appliances and low output power levels, for medium output power half bridge inverter is used. Finally, the full bridge inverter is used for high output power levels. The proposed converter uses a half bridge boost rectifier and a full bridge inverter for obtaining high output power levels. As result converter efficiency is improved and low frequency ac current do not flow through IH load, thus reducing peak current and conduction losses. Full bridge topology has dual –IGBT(Insulated gate bipolar transistor) inverter modules to convert AC sine wave so that the high frequency current waveform is complete, clear and stable and due to the high current allocation efficiency of full bridge topology it can electronically transfer high thermal efficiency and can load high inductive loads.

II. Direct Ac-Ac Converter

An ac-ac converter converts a fixed voltage, fixed frequency ac input supply to obtain a variable voltage ac output. In ac-ac converter sinusoidal input currents and bidirectional power flow can be achieved by coupling a pulse width modulation (PWM) rectifier and a PWM inverter to a dc link. The DC link quantity is an energy storage element which is a capacitor C for the voltage dc link and inductor for the current dc link.Here we propose a direct ac-ac converter for achieving high frequency application. In conventional design due to the input current harmonics, a low frequency power get across the load. So it take less heating time. The need of an additional filter requirement is eliminated in the proposed model. Due to the activation of more than one diode on each half cycle leads to high conduction loss in case of an conventional converter. Direct ac-ac converter is free from the conduction loss.

III. Block Diagram

The fig.1 Shows the block diagram of Direct AC-AC Full Bridge Converter. The input ac voltage is fed to AC-AC full bridge converter. Controller provides necessary gate signal for the driver circuit. Driver circuit fix duty ratio of these pulses and it is driven to the resonant converter. Thus a high frequency ac voltage is fed to the induction pan.

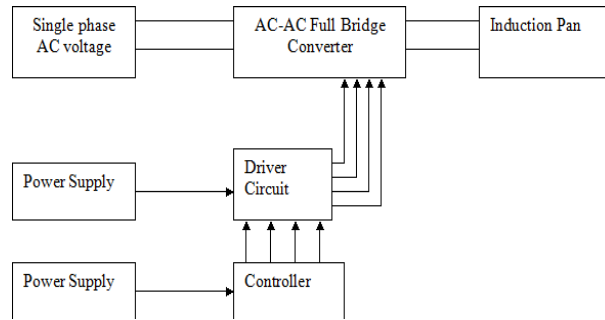


Fig. 1. Block diagram of direct ac-ac full bridge converter

IV. Circuit Diagram

The circuit diagram of direct ac-ac converter is shown in fig.2. We propose full bridge ac-ac converter here which is more efficient than half bridge converter.

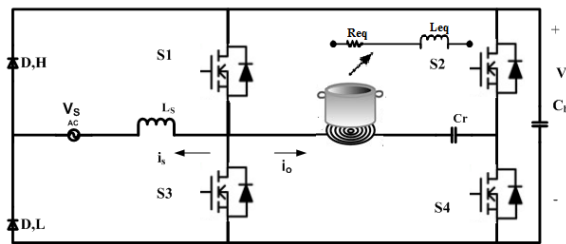


Fig. 2. Circuit diagram of direct ac-ac converter

The converter consists of input AC voltage V_s , four IGBT switches S1, S2, S3, and S4, dc-link capacitor C, input inductor L_s , diodes D,H D,L and IH load. The AC power supply is rectified by the half-wave rectifier branch composed of D,H and D,L. The full bridge inverter circuit consists of four switches with anti-parallel diodes S1, S2, S3 and S4. The switches S1 and S2 is used both to perform a boost dc–dc conversion of the mains ac voltage and additionally, to supply the high frequency current to the inductor along with S3 and S4. The voltage boost is performed by means of the input inductor L_s and the dc-link capacitor C_b . The IH load is modelled as a series of equivalent RL circuit composed of R_{eq} and L_{eq} . The input inductor L_s and the dc-link capacitor C_b boost the voltage. The boost dc-dc conversion of mains AC voltage is done by switches S1 and S3. The wave form of the boost dc-dc circuit is shown in fig.3.

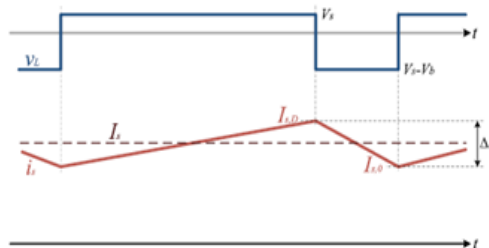


Fig. 3. Wave form of the boost dc-dc circuit

V. Ac-Ac Full Bridge Converter Analysis

The direct AC - AC converter operation can be analyzed through the four modes of operation through the equivalent circuit I to IV. During the positive half cycle of the main voltage D_H conducts that is indicated by the two stages I and II. Where as D_L conducts during the negative half cycle that is indicated by the stage III and IV. This shows that only one rectifier is activated at a time, that will reduce conduction loss as it is compared to the conventional converter. The Zero voltage switching is guaranteed in it by providing time delay between the switching of each switch. The supply voltage is applied across the inductor during the modes I and III and the dc link capacitor is charged by the inductor current during mode II and IV. The rectification for positive supply voltage is done by switch S1 and for the negative cycle, its done by switch S2. The required high frequency AC current i_o to supply the IH load is performed by the inverter branch composed of S1, S2, S3 and S4. The equivalent full bridge inverter is supplied by the dc link capacitor voltage V_b . The total current in the device is expressed as the sum of the boost stage and the equivalent full bridge inverter stage.

equivalent full bridge series resonant inverter. In mode1 S3 and S4 are turned on, D,H conducts during positive half cycle of current dc link capacitor is charged by the supply voltage and input inductor L_s . The high frequency current is applied across the load. Mode 1 is shown in figure .S1 and S4 are turned on during mode 2, supply voltage is applied to the input inductor L_s . In mode 3 S1 and S2 are turned on D,L conducts and supply voltage and charge stored in the input inductor charges the dc link capacitor C_b . S3 and S2 conducts during mode 4 and charges input inductor L_s . Modes 2, 3, 4 are shown in figures 5, 6, 7 respectively. There are many advantages of direct converter over conventional converter that the output frequency of direct AC - AC converter is higher because of the input current is free from harmonics.

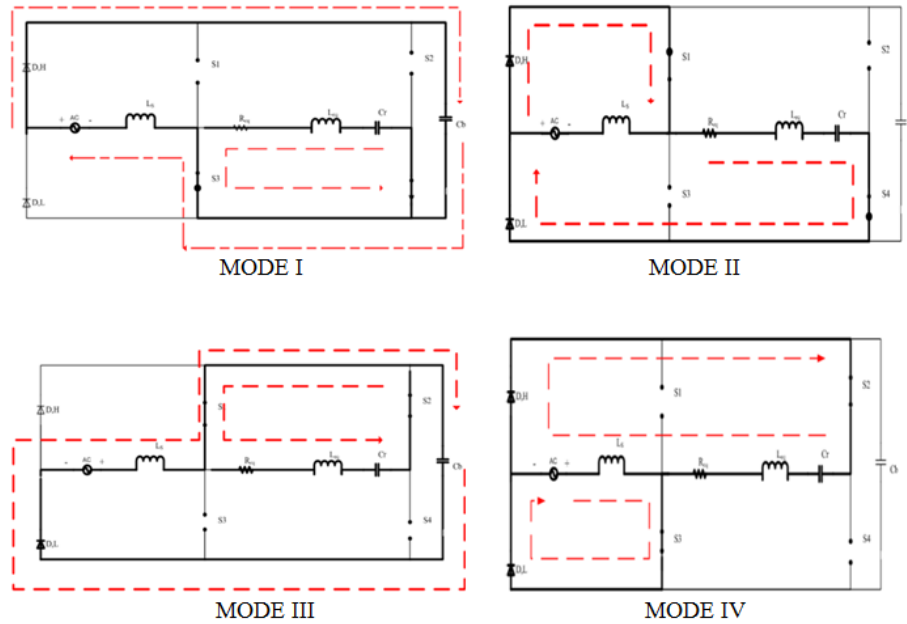


Fig. 4. Modes of operation

VI. Design Of Full Bridge Converter

The input voltage is boosted by the input inductor L_s and dc link capacitor C_b . The switch S1 and S3 perform the boost dc to dc conversion. The dc–dc boost circuit waveform are shown in Fig.7,where the steady state average input current is I_s . In order to avoid the current ripples and high frequency current through the rectifier diode, a continuous current mode is assumed. In steady state average voltage across inductor is zero.

$$V_s DT_{sw} + (V_s - V_b)(1-D)T_{sw} = 0 \tag{1}$$

The ratio of voltage conversion is same as the boost converter.

$$\frac{V_b}{V_s} = \frac{1}{1 - D} \tag{2}$$

The input current of the temporal waveform can be calculated using the input current ripple ΔI_s ,

$$\Delta I_s = I_{s,D} - I_{s,0} = \frac{V_s}{L_s} DT_{sw} \tag{3}$$

Where the minimum and maximum input current values during a switching period is $I_{s,0}$ and $I_{s,D}$ respectively. Consequently, the input current of the temporal waveform i_s results

$$I_s(t) = \begin{cases} \left(I_s - \frac{\Delta I_s}{2} \right) + \frac{\Delta I_s}{DT_{sw}} t, (0 \leq t < DT_{sw}) \\ \left(I_s + \frac{\Delta I_s}{2} \right) - \frac{\Delta I_s}{DT_{sw}} (t - DT_{sw}), (DT_{sw} \leq t < T_{sw}) \end{cases} \tag{4}$$

The condition of CCM is satisfied as ,

$$CCM \Rightarrow I_{s,0} > 0 \Rightarrow I_s > \frac{\Delta I_s}{2} \quad (5)$$

The input power P_{in} for an unity power factor is assumed

$$I_s > \frac{\Delta I_s}{2} \Leftrightarrow \frac{P_{in}}{V_s} > \frac{V_s}{2L_s} DT_{sw} \quad (6)$$

that will lead to,

$$P_{in} > \frac{V_s^2}{2L_s} DT_{sw} \quad (7)$$

So there is a trade off between the input power, the input inductor value, and modulation parameters for a given supply voltage level to ensure CCM. The full bridge series resonant inverter consists of four switches S1, S2, S3 and S4. It is supplied by the output voltage of the boost stage V_b . By using the Fourier harmonic analysis, the output power P_o results

$$\begin{aligned} P_o &= \sum_{h=0}^{\infty} R_{eq} \frac{V_{0h,rms}^2}{Z_{o,h}^2} = \sum_{h=0}^{\infty} R_{eq} \frac{\frac{1}{2} V_{o,h}^2}{Z_{o,h}^2} \\ &= \sum_{h=0}^{\infty} \frac{R_{eq}}{2} \frac{V_{o,h}^2}{R_{eq}^2 + \left(2\pi f_{sw} h L_{eq} - \frac{1}{2\pi f_{sw} h C_r} \right)^2} \end{aligned} \quad (8)$$

where h is the harmonic number, Z_o is the impedance of the series RLC resonant tank, and V_o is the output voltage of the inverter. Its sinusoidal peak voltage results

$$\hat{V}_{o,h} = \frac{V_b}{h\pi} \sqrt{a_h^2 + b_h^2} \quad (9)$$

Where coefficients of fourier series are,

$$a_h = \sin(2\pi h D), b_h = 1 - \cos(2\pi h D)$$

The output power results,

$$P_o = \sum_{h=0}^{\infty} \frac{(1 - \cos(2\pi h D))}{(h\pi(1-D))^2} \frac{R_{eq} V_s^2}{R_{eq}^2 + \left(2\pi f_{sw} h L_{eq} - \frac{1}{2\pi f_{sw} h C_r} \right)^2} \quad (10)$$

VII. Simulation Of Full Bridge Converter

The simulation of full bridge converter is obtained in MATLAB. Simulation diagram is shown in fig.5. The high frequency output voltage across the load is shown in fig.6

Table I: Design Parameters

Parameters	Values
Input voltage	325.2V
Switching frequency, f_s	150KHz
Equivalent load resistance, R_{eq}	98.9Ω
Equivalent load inductance, L_{eq}	155μH
Input inductor, L_s	900μH
DC link capacitor C_b	470pF
Resonant capacitor	8.56nF

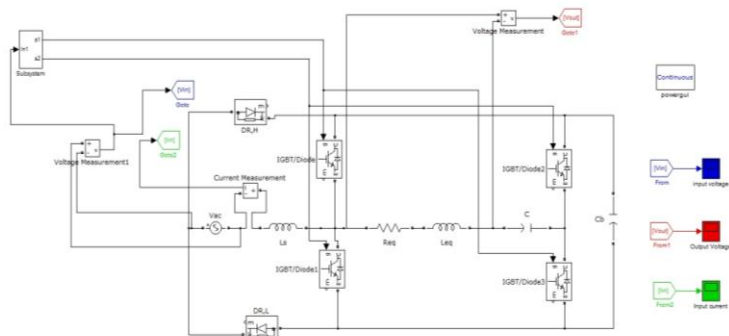


Fig. 5. Simulation of full bridge converter

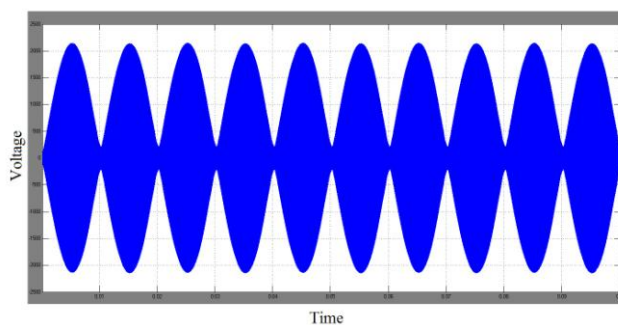


Fig. 6. Output Voltage waveform

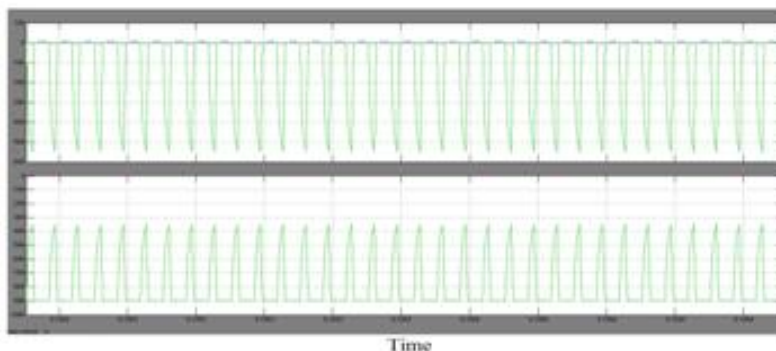


Fig. 7. Voltage & current across Diode

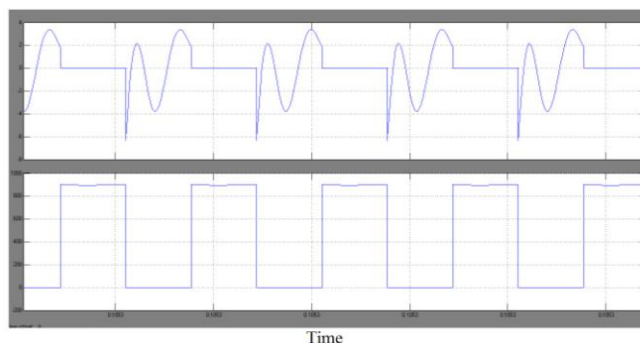


Fig. 8. Voltage & current across IGBT

The voltage and current across Diode is shown in Fig.7. There is only one diode is activated at a time so it will reduce conduction loss. Due to the reduction of non-linear load in the input, input current is free from harmonics. The Voltage and current across IGBT are shown in fig.8. S1 and S3 conducts 50 percent during positive half cycle. S2 and S4 conducts 50 percent during negative half cycle.

VIII. Hardware Platform



Fig.9. Hardware setup

Table II: Hardware Requirement

Components	Specification
IGBT	IRF540
Driver IC	TLP 250
Capacitor	470nF
Diodes	IN4007
Inductor	155 μ H
Microcontroller	ATMEGA 16

In hardware section consist of a Rectifier section inverter section (H-Bridge inverter), driver circuit, Microcontroller for giving pulse, and an induction coil. The Switching pulse for to switch corresponding IGBT are obtained in DSO are shown in fig.10

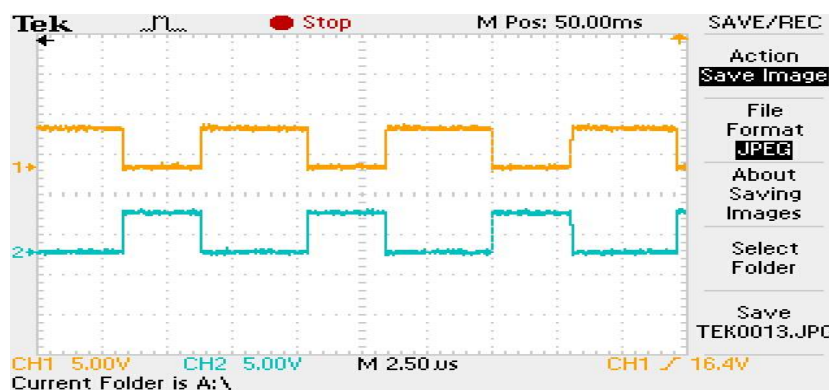


Fig. 10. Switching pulse

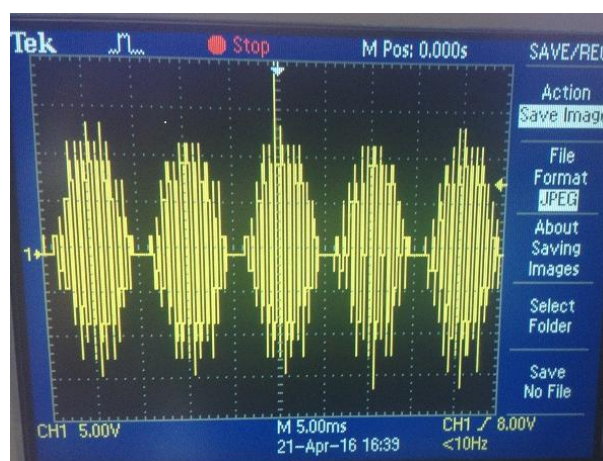


Fig. 11. Output Voltage

IX. Conclusion

A full bridge AC-AC resonant converter for induction heating application is proposed here. The key features of the proposed converter are reduced component count and high efficiency. The converter can operate with zero voltage switching. As a result power converter is improved in whole operating range. The hardware is presented using AVR microcontroller as a prototype model.

References

- [1]. Hector Sarnago, *Student Member, IEEE*, Oscar Lucia, *Member, IEEE*, Arturo Mediano, *Senior Member, IEEE*, and Jos'e M. Burdio, *Senior Member, IEEE*, Direct AC-AC Resonant Boost Converter for Efficient Domestic Induction Heating Applications, *IEEE TRANSACTIONS ON POWER ELECTRONICS*, VOL. 29, NO. 3, MARCH 2014.
- [2]. Sarnago, O. Lucia Gil, A. Mediano, and J. M. Burdio, "Modulation scheme for improved operation of an RB-IGBT-based resonant inverter applied to domestic induction heating," *IEEE Trans. Ind. Electron.*, vol. 60, no. 5, pp. 2066–2073, May 2013.
- [3]. O. Lucia, J. M. Burdio, I. Mill'an, J. Acero, and D. Puyal, "Load-adaptive control algorithm of half-bridge series resonant inverter for domestic induction heating," *IEEE Trans. Ind. Electron.*, vol. 56, no. 8, pp. 3106–3116, Aug. 2009.
- [4]. O. Lucia, J. M. Burdio, I. Mill'an, J. Acero, and L. A. Barragan, "Efficiency oriented design of ZVS half-bridge series resonant inverter with variable frequency duty cycle control," *IEEE Trans. Power Electron.*, vol. 25, no. 7, pp. 1671–1674, Jul. 2010.
- [5]. Yilmaz, M. Ermis, and I. Cadirci, "Medium-frequency induction melting furnace as a load on the power system," *IEEE Trans. Ind. Appl.*, vol. 48, no. 4, pp. 1203–1214, Jul./Aug. 2012.
- [6]. J. Egalon, S. Caux, P. Maussion, M. Souley, and O. Pateau, "Multiphase system for metal disc induction heating: Modeling and RMS current control," *IEEE Trans. Ind. Appl.*, vol. 48, no. 5, pp. 1692–1699, Sep./Oct. 2012.
- [7]. Millan, J. M. Burdio, J. Acero, O. Lucia, and S. Llorente, "Series resonant inverter with selective harmonic operation applied to all-metal domestic induction heating," *IET Power Electron.*, vol. 4, no. 5, pp. 587–592, May 2011.
- [8]. O. Lucia, J. M. Burdio, J. I. Mill'an, J. Acero, and L. A. Barragan, "Efficiency-oriented design of ZVS half-bridge series resonant inverter with variable frequency duty cycle control," *IEEE Trans. Power Electron.*, vol. 25, no. 7, pp. 1671–1674, Jul. 2010.